

ELECTRICAL SPECIFICATIONS:

NOTES

1.0 TURNS RATIO: (P6-P5-P4) : (J6-J3) (P3-P2-P1) : (J2-J1)

: 1CT : 1CT± 3% : 1CT : 1CT ± 3% 1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

2.0 INDUCTANCE: (P6-P4) (P3-P1)

: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 LEAKAGE INDUCTANCE: P6-P4 (WITH J6 AND J3 SHORT) P3-P1 (WITH J2 AND J1 SHORT)

: 0.3 MAX. @ 1MHz : 0.3 MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P6,P5,P4) TO (J6,J3) (P3,P2,P1) TO (J2,J1)

: 30pf MAX @ 1MHz : 30pf MAX. @ 1MHZ

5.0 DC RESISTANCE: (J6-J3)=(J2-J1)

: 1.2 ohms Max.

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6.0 RETURN LOSS: 1MHz TO 30MHz : 18dB MIN. 60MHz TO 80MHz : 12dB MIN.

NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3).

7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3) : 1500 VAC (J3, J6) TO (P4,P6) : 1500 VAC

8.0 INSERTION LOSS: RS=RL=100 ohms

100KHz TO 100MHz : 1.1 dB TYP

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS

OUTPUT VOLTAGE = 1 V peak: 3.0 nS MAX PULSE WIDTH= 112nS : 3.0 nS MAX

10.0 CROSS TALK: 1MHz TO 100MHz : 40 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION: 30MHz TO 100MHz : 35dB TYP

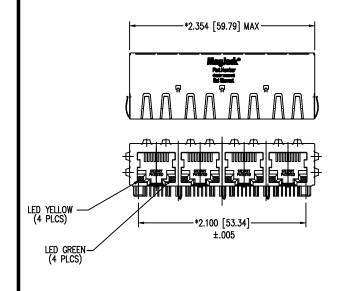
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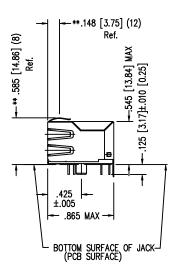
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NOTES:

- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS
- DIMENSIONS SHOWN WITH "*" TO BE CENTRAL ABOUT CENTER LINE
- "**" ON DIMENSION INDICATES HIGHEST POINT OF BEAM
- DIMENSIONS SHOWN ARE SUBJECT TO CHANGE WITHOUT NOTICE
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
 SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- STANDARD 50 MICRO-INCH SELECTIVE

LED SPECIFICATIONS

GREEN YELLOW FORWARD VOLTAGE(20mA): 2.2v (MAX) 2.5v (MAX) FORWARD VOLTAGE(20mA): 2.1v (TYP) 2.2v (TYP) POWER DISSIPATION: 105mW 105mW WAVE LENGTH: 590nm 565nm INTENSITY @ 10mA: 2-8 MCD 8-32 MCD

.050 [1.27] TYP TOL NON-ACCUM .550 [13.97] TYP-.025 [0.63] -.100 [2.54] TYP ø .035 [0.89] (32) TOL NON-ACCUM ø.066 [1.68] (3) PIN 1 GO O O O · ø.040 [1.02](16) 090 [2.28] 160 [4.06] -*2.100 [53.34] - *.309 [7.85]TYP. --*.509 [12.93]TYP. P.C.B. RECOMMENDED HOLE LAYOUT SEEN FROM COMPONENT SIDE TOLERANCE ±.003 [0.08] UNLESS OTHERWISE SPECIFIED

.275 [6.99]-

⊢.025 [0.63]

-.550 [13.97] TYP TOL NON-ACCUM

CT720091/CT720074/24-0028

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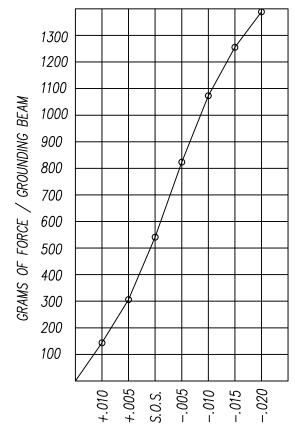
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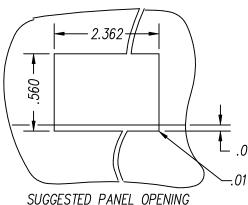
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DRAWING NO.

SI-40143 REV



PANEL GROUNDING BEAM DEFLECTION S.O.S. = SUGGESTED OPENING SIZE



POINT OF CONTACT WITH PANEL - .080 .275 MAX -

> THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .005 ON THE ВОТТОМ.

.000 (TOP OF PCB TO BOTTOM OF OPENING)

-.010 MAX. RADIUS(4)

CT720035X1/24-001701

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DRAWING NO.

SI-40143 REV. 06