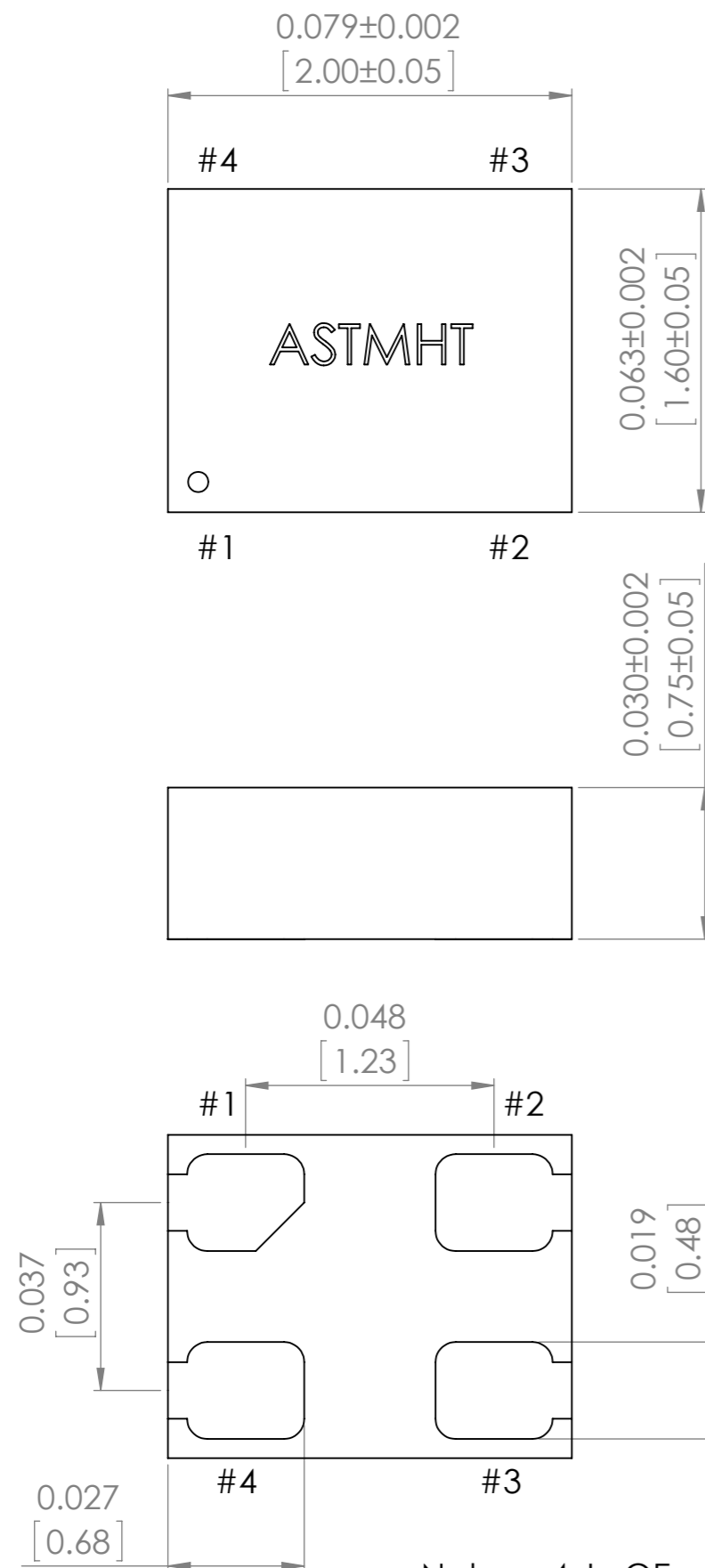
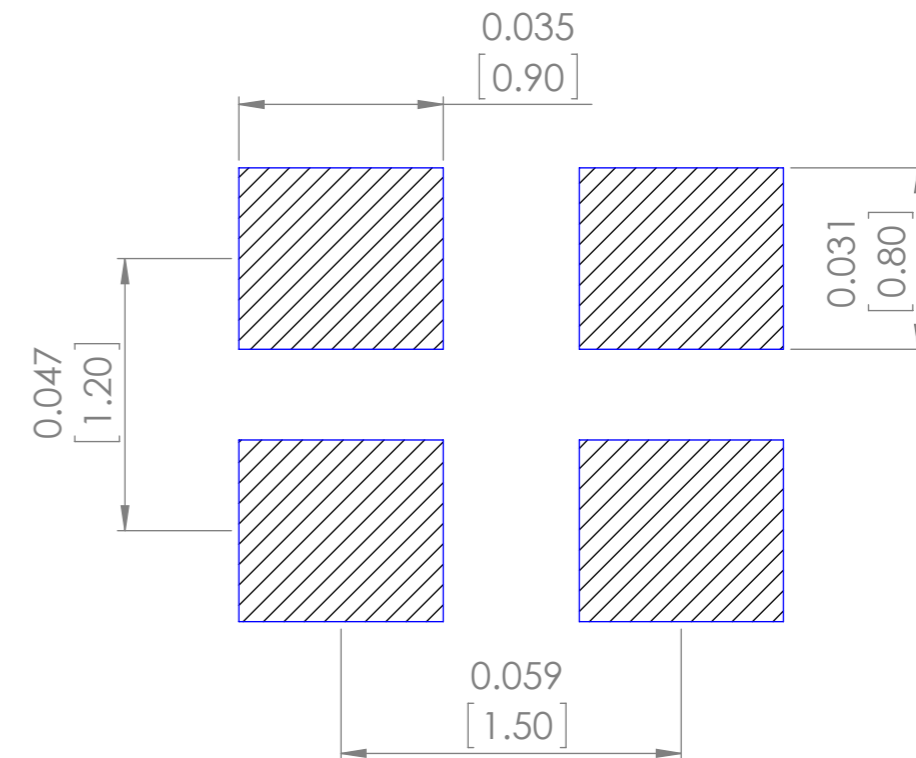


## 2.0 x 1.6mm Package (Option "A")

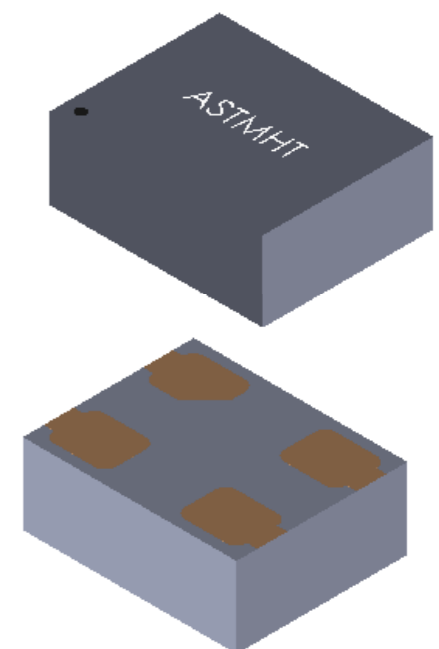


- Notes:
- In OE or  $\overline{ST}$  mode, a pull-up resistor of 10kΩ or less between OE/ $\overline{ST}$  pin and Vdd is recommended if pin 1 is not externally driven. If pin 1 needs to be left floating, use the NC option.
  - A capacitor value of 0.1μF or higher between Vdd and GND is required.


## Recommended Land Pattern



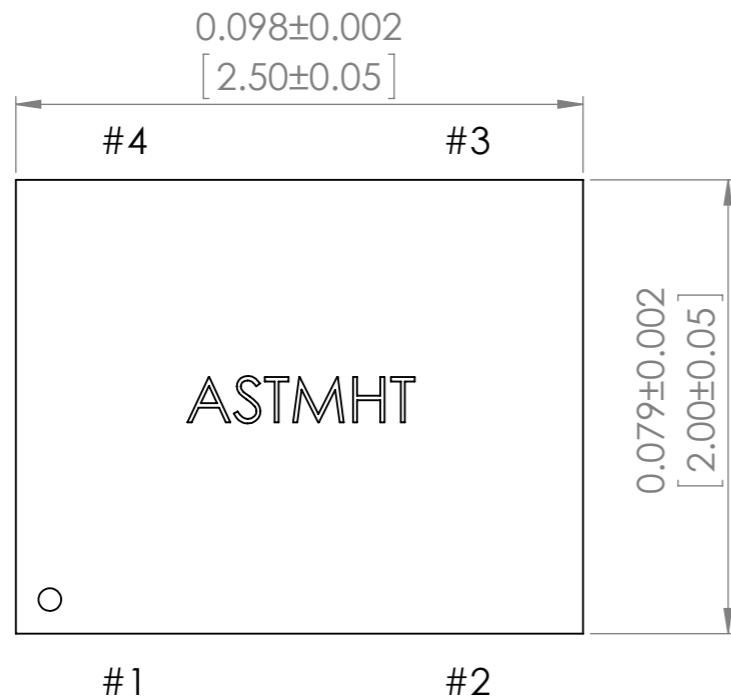
Pin	Name	Functionality	
1	Output Enable	H or Open (4): specified frequency output L: output is high impedance. Only output driver is disabled	
	Standby	H or Open (4): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.	
	No Connect	Any voltage between 0 and Vdd or open (4): specified frequency. pin 1 has no function	
2	GND	Power	Electrical ground (5)
3	Out	Output	Oscillator clock output
4	Vdd	Power	Power supply voltage (5)



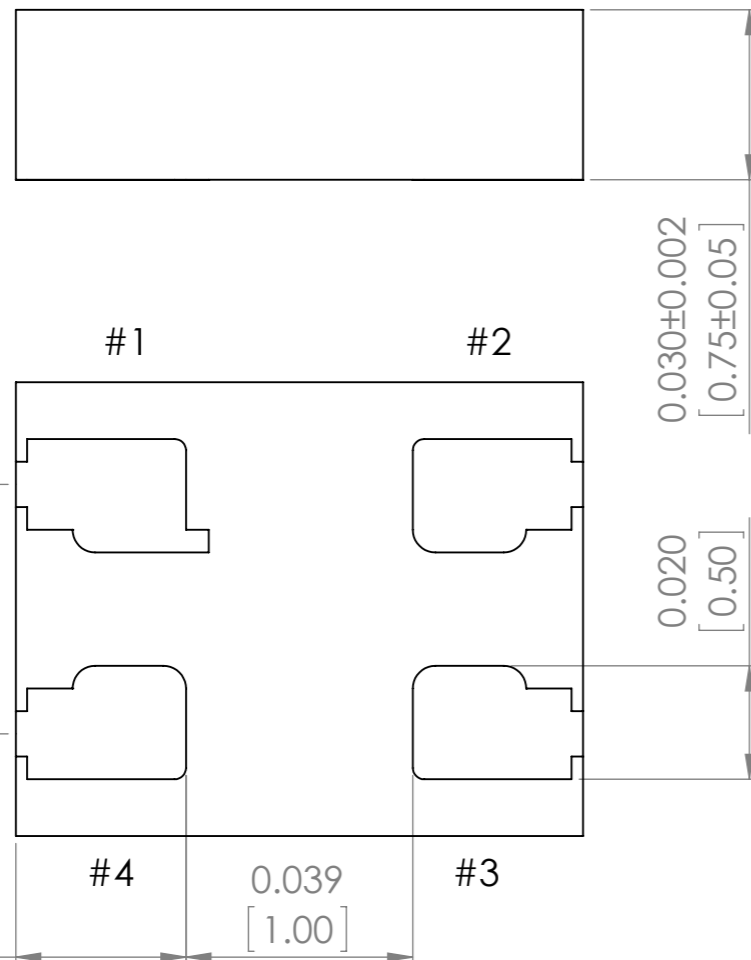
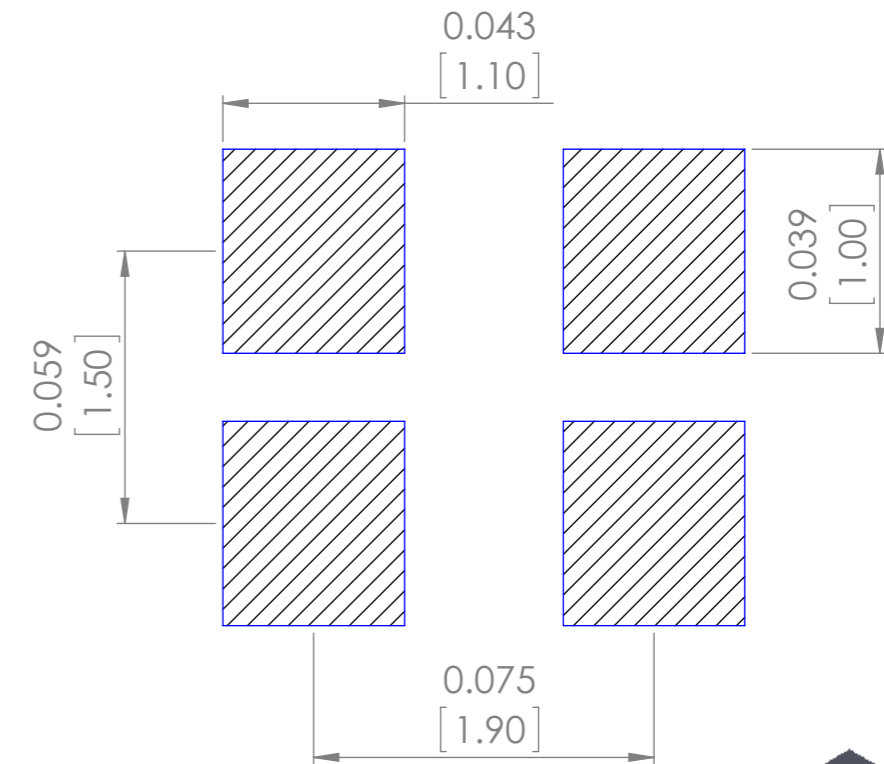
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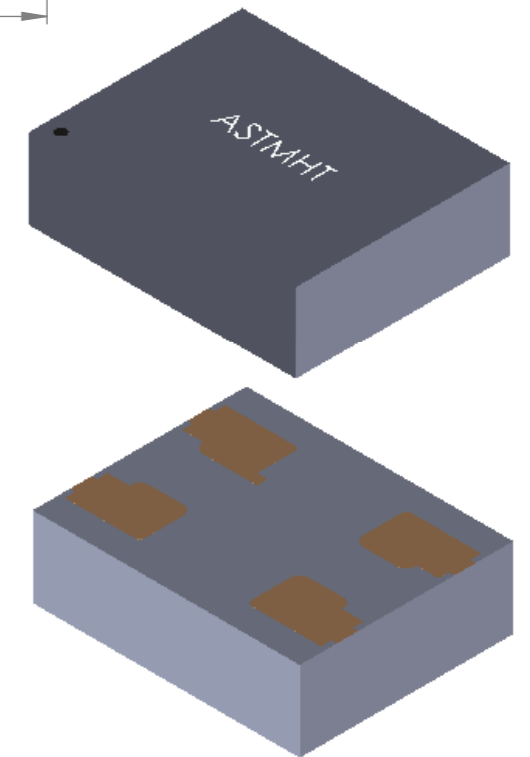
## 2.5 x 2.0mm Package (Option "D")




### Recommended Land Pattern



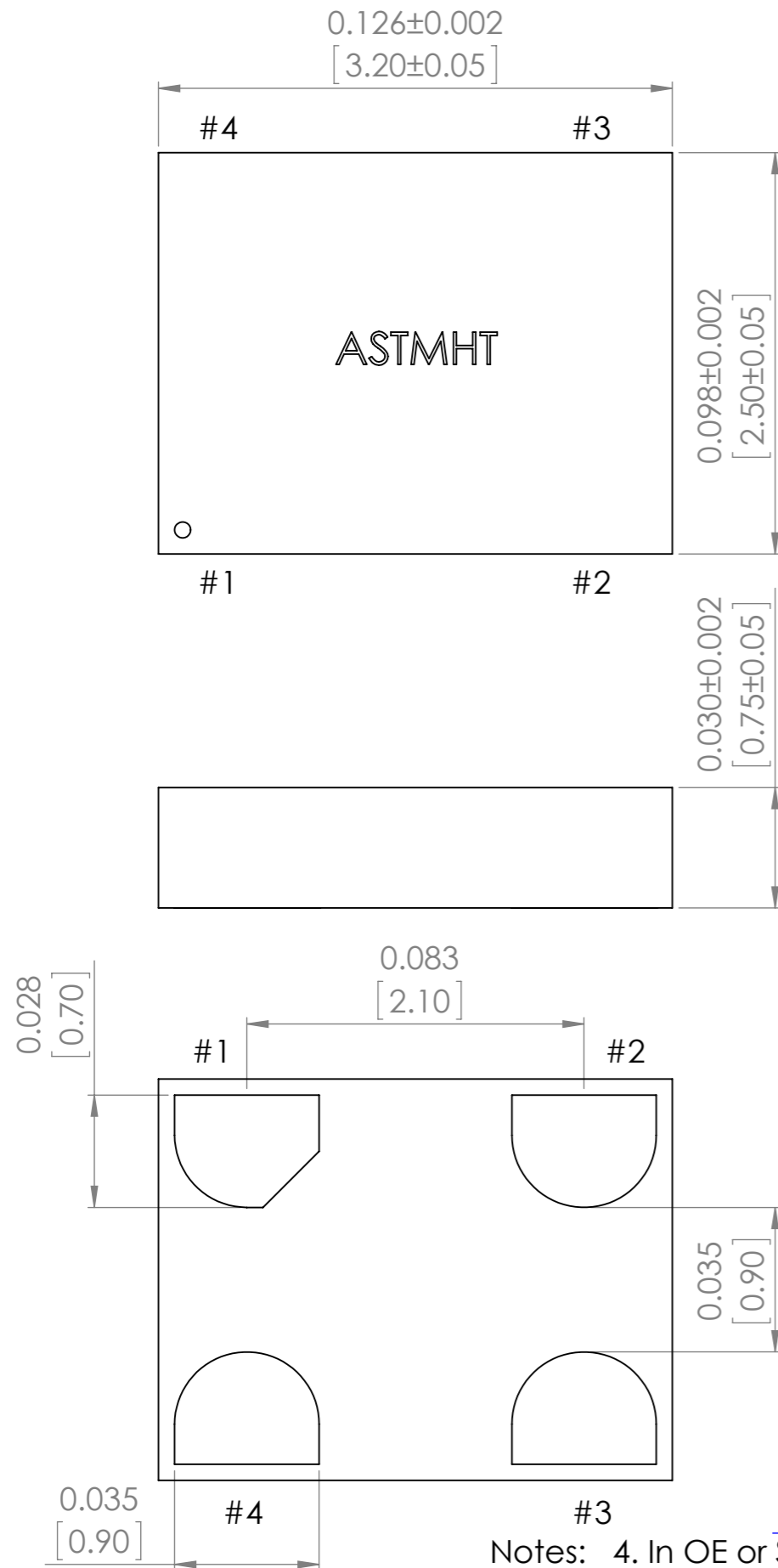
Pin	Name	Functionality	
1	Output Enable	H or Open (4): specified frequency output L: output is high impedance. Only output driver is disabled	
	Standby	H or Open (4): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.	
	No Connect	Any voltage between 0 and Vdd or open (4): specified frequency. pin 1 has no function	
2	GND	Power	Electrical ground (5)
3	Out	Output	Oscillator clock output
4	Vdd	Power	Power supply voltage (5)



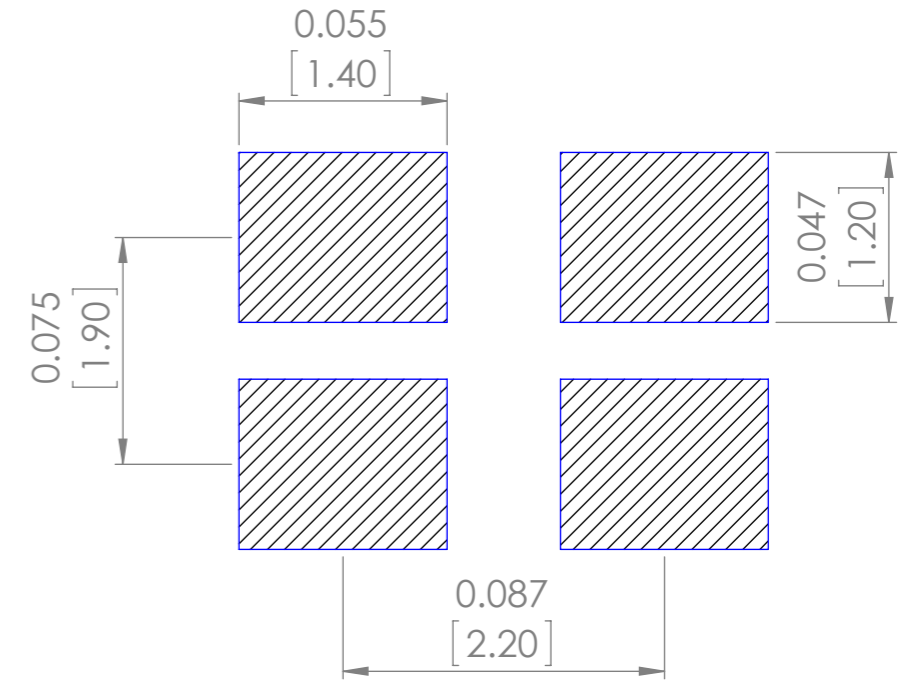
- Notes:
- In OE or  $\overline{ST}$  mode, a pull-up resistor of 10kΩ or less between OE/ $\overline{ST}$  pin and Vdd is recommended if pin 1 is not externally driven. If pin 1 needs to be left floating, use the NC option.
  - A capacitor value of 0.1μF or higher between Vdd and GND is required.

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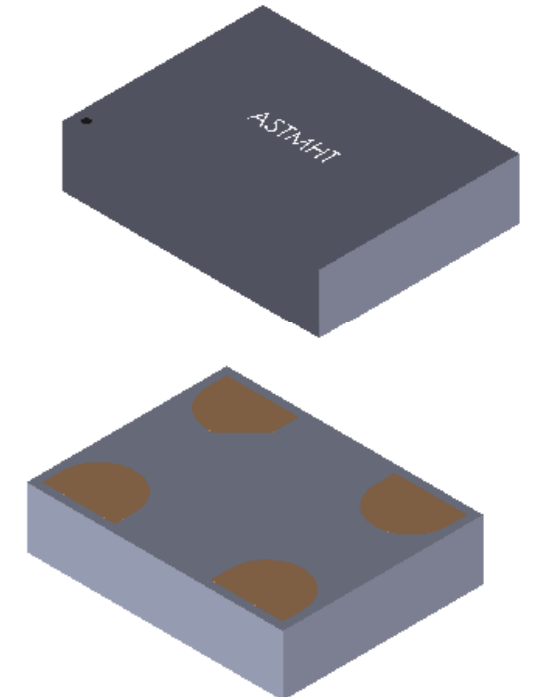
### 3.2 x 2.5mm Package (Option "E")




### Recommended Land Pattern



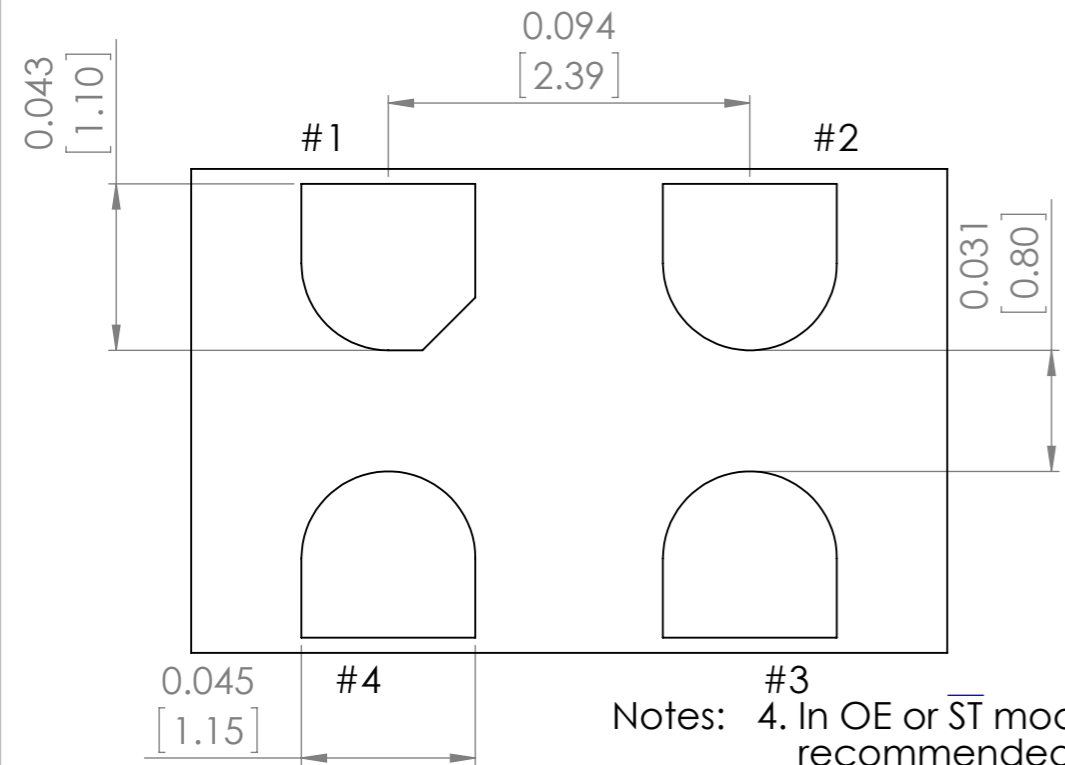
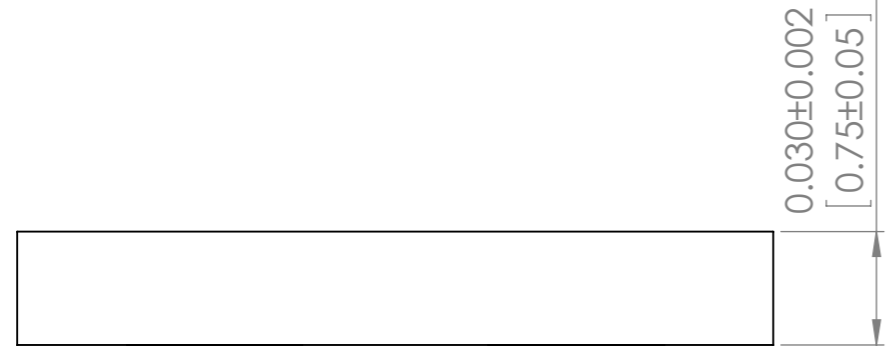
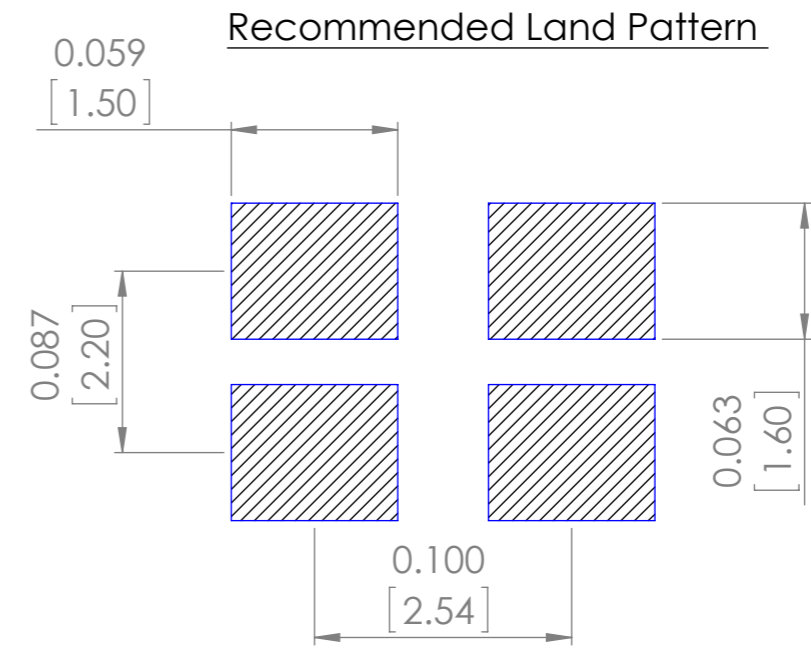
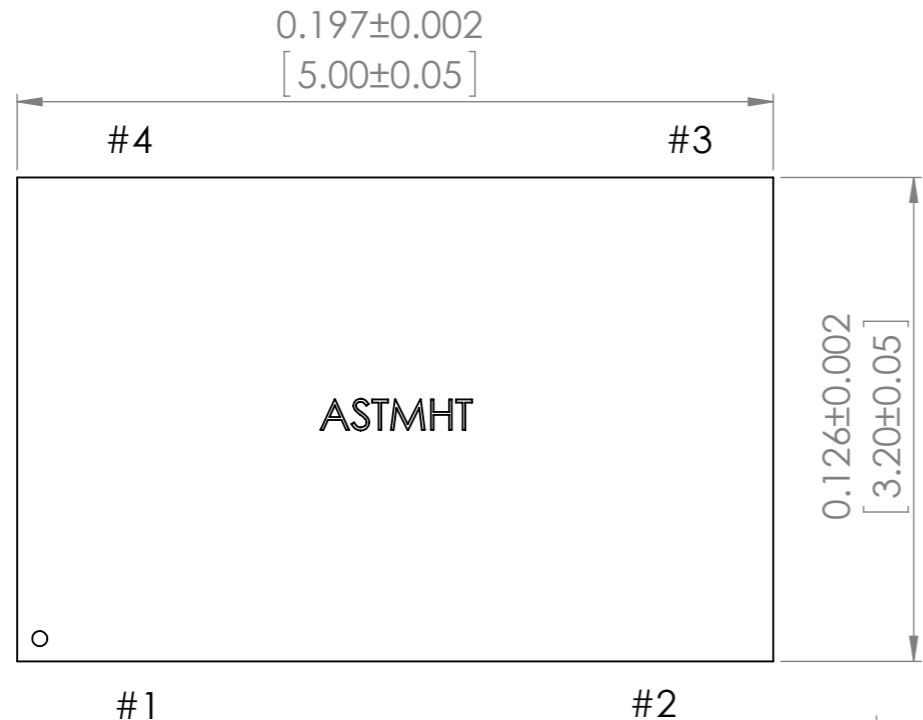
Pin	Name	Functionality
1	OE/ST	Output Enable H or Open (4): specified frequency output L: output is high impedance. Only output driver is disabled
	Standby	H or Open (4): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
	No Connect	Any voltage between 0 and Vdd or open (4): specified frequency. pin 1 has no function
2	GND	Power Electrical ground (5)
3	Out	Output Oscillator clock output
4	Vdd	Power Power supply voltage (5)



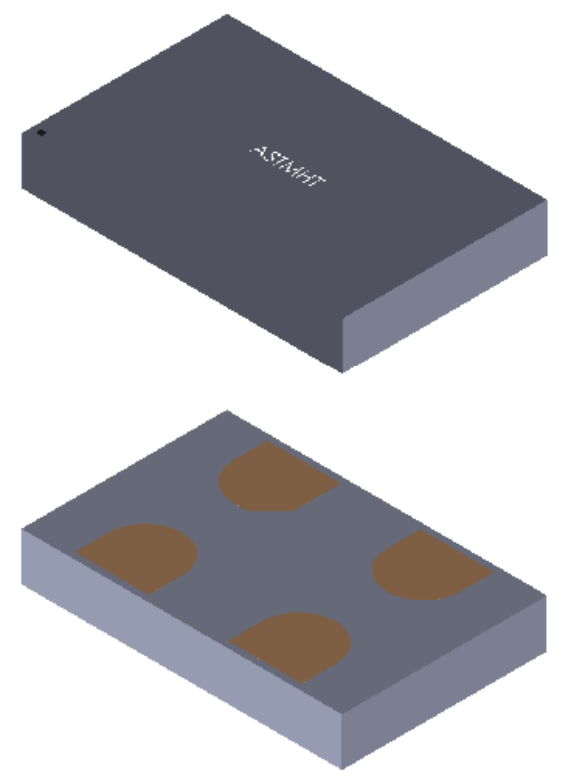
Notes: 4. In OE or ST mode, a pull-up resistor of 10kΩ or less between OE/ST pin and Vdd is recommended if pin 1 is not externally driven. If pin 1 needs to be left floating, use the NC option.  
5. A capacitor value of 0.1μF or higher between Vdd and GND is required.

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5.0 x 3.2mm Package (Option "FL")



Pin	Name	Functionality
1	Output Enable	H or Open (4): specified frequency output L: output is high impedance. Only output driver is disabled
	Standby	H or Open (4): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
	No Connect	Any voltage between 0 and Vdd or open (4): specified frequency. pin 1 has no function
2	GND	Power Electrical ground (5)
3	Out	Output Oscillator clock output
4	Vdd	Power Power supply voltage (5)



Notes: 4. In OE or ST mode, a pull-up resistor of 10kΩ or less between OE/ST pin and Vdd is recommended if pin 1 is not externally driven. If pin 1 needs to be left floating, use the NC option.  
5. A capacitor value of 0.1μF or higher between Vdd and GND is required.

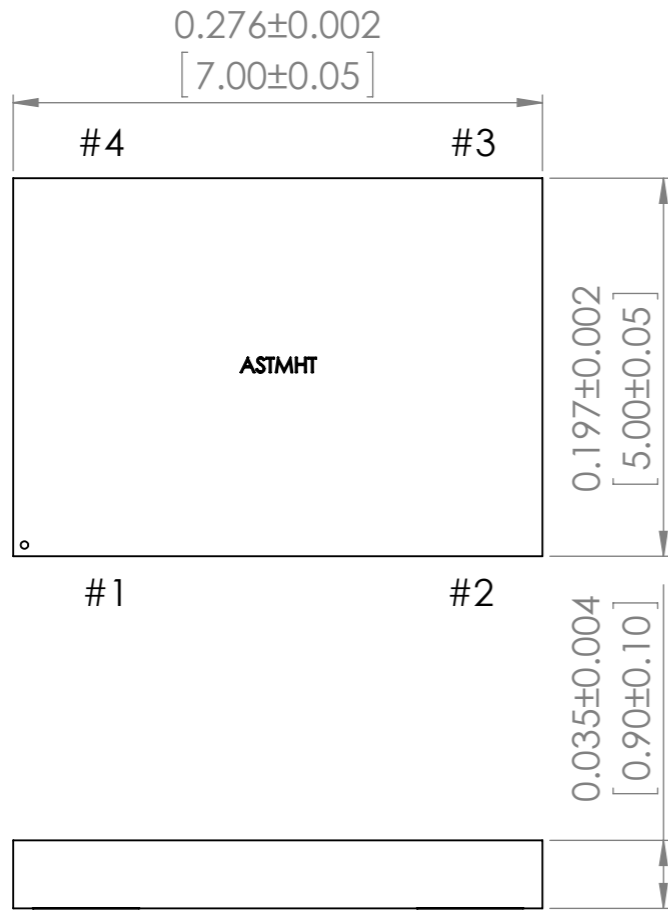
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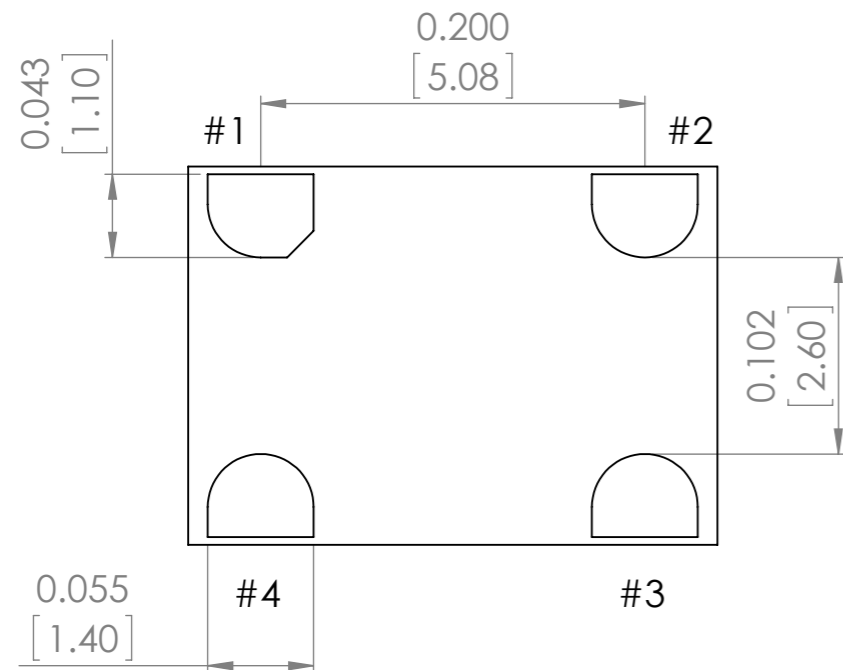
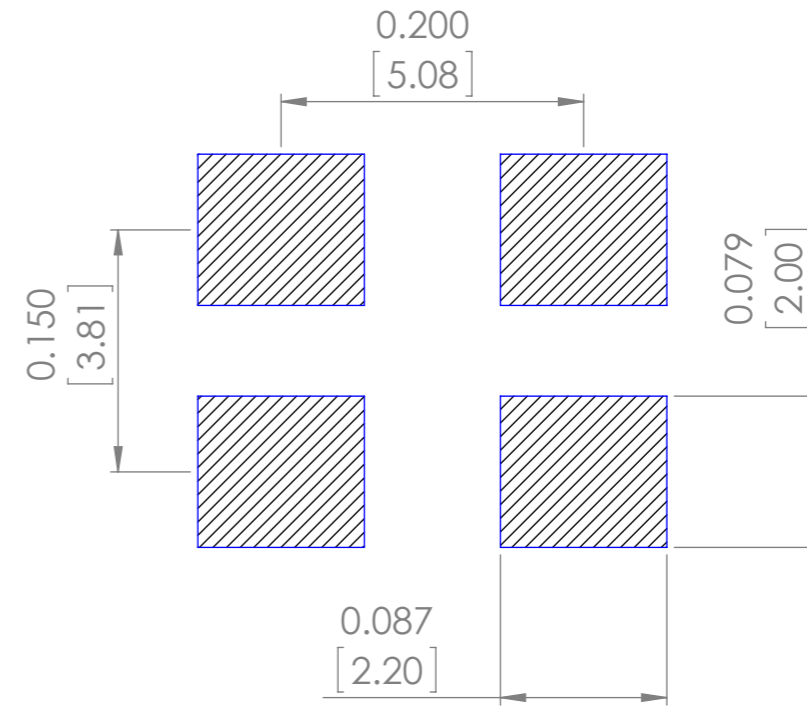
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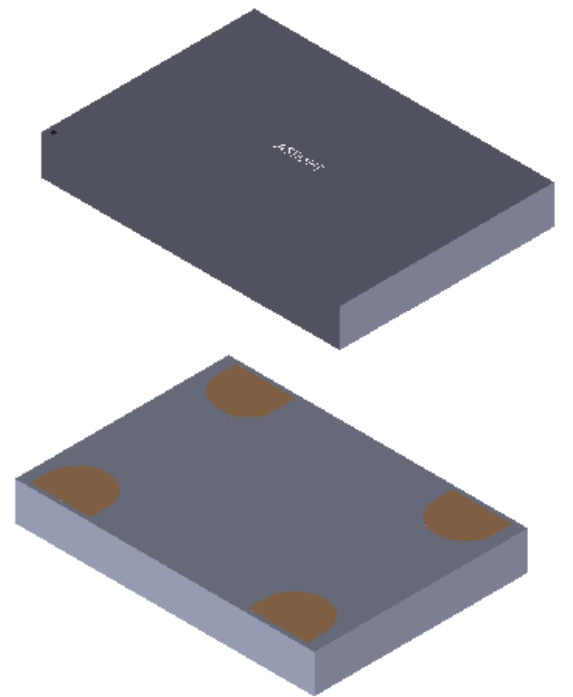
**7.0 x 5.0mm Package (Option "V")**



**Recommended Land Pattern**




Pin	Name	Functionality	
1	Output Enable	H or Open (4): specified frequency output L: output is high impedance. Only output driver is disabled	
	Standby	H or Open (4): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.	
	No Connect	Any voltage between 0 and Vdd or open (4): specified frequency. pin 1 has no function	
2	GND	Power	Electrical ground (5)
3	Out	Output	Oscillator clock output
4	Vdd	Power	Power supply voltage (5)



- Notes: 4. In OE or  $\overline{ST}$  mode, a pull-up resistor of 10k $\Omega$  or less between OE/ $\overline{ST}$  pin and Vdd is recommended if pin 1 is not externally driven. If pin 1 needs to be left floating, use the NC option.  
5. A capacitor value of 0.1  $\mu$ F or higher between Vdd and GND is required.

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