

**CRYSTAL OSCILLATOR (SPXO) OUTPUT: CMOS, TTL** 

SG - 645/SG - 636 series

 Frequency range : 2.21675 MHz to 135 MHz

 Supply voltage : 2.5 V Typ. / 3.3 V Typ. / 5.0 V Typ. Function Output enable(OE) or Standby(ST) •External dimensions :  $7.1 \times 5.1 \times 1.5 \text{ mm}$  (t: Max.)···SG-645

 $10.5 \times 5.8 \times 2.7 \text{ mm (t: Max.)} \cdots \text{SG-636}$ 



Product Number (please contact us) SG-645 : Q33645xx2xxxx00





Actual size

SG-645 series



## Specifications (characteristics)

		Specifications			
Item	Symbol	SG-636 PTF	SG-636 PCE SG-636 SCE	SG-636 PDE	Conditions / Remarks
Output frequency range	fo	2.21675 MHz to 41.000 MHz	2.21675 MHz to 40.000 MHz	2.21675 MHz to 40.000 MHz	Please contact us about available frequencies.
Supply voltage	Vcc	5.0 V ±0.5 V	3.3 V ±0.3 V	2.5 V ±0.25 V	
Storage temperature	T_stg		-55 °C to +100 °C		Storage as single product.
Operating temperature	T_use		-20 °C to +70 °C		
Frequency tolerance	f tol		C: ±100 × 10 <sup>-6</sup>		-20 °C to +70 °C
Current consumption	Icc	17 mA Max.	9 mA Max.	5 mA Max.	No load condition
Disable current	I_dis	10 mA Max.	5 mA Max.	3 mA Max.	OE=GND
Stand-by current	I_std	_	2 μA Max.	_	ST =GND(SCE)
Symmetry	SYM	40 % to 60 % 45 % to 55 %	45 % to 55 %		CMOS load:50 % Vcc level TTL load: 1.4 V level
Output voltage	Vон	Vcc-0.4 V Min.			IOH=-8 mA(PTF) / -4 mA(SCE,PCE) / -3.2 mA(PDE)
	Vol	0.4 V Max.			loL=16 mA(PTF) / 4 mA(SCE,PCE) / 3.2 mA(PDE)
Output load condition (TTL)	L_TTL	10 TTL Max.	_		L_CMOS ≤ 15 pF
Output load condition (CMOS)	L_CMOS	50 pF Max.	30 pF Max.	15 pF Max.	
Input voltage	VIH	2.0 V Min.	80 % Vcc Min.		OE Terminal or ST Terminal (SCE)
	VIL	0.8 V Max.	20 % Vcc Max.		- '
Rise time / Fall time	tr / tf	7 ns Max.	5 ns Max.		CMOS load:20 % Vcc to 80 % Vcc level
	LI / LI	5 ns Max. —		TTL load:0.4 V to 2.4 V level	
Start-up time	t_str	4 ms Max.	4 ms Max.		Time at minimum supply voltage to be 0 s
Frequency aging	f_aging		$\pm 5 \times 10^{-6}$ / year Max.		+25 °C, Vcc=5.0 V/3.3 V/2.5 V, First year

### Specifications (characteristics)

		Specifications			
Item	Symbol	SG-636 PTG	SG-636 PHG	SG-636 PCG SG-636 SCG	Conditions / Remarks
Output frequency range	fo	2	2.21675 MHz to 33.000 MH	z *1	Please contact us about available frequencies.
Supply voltage	Vcc	4.5 V	to 5.5 V	2.7 V to 3.6 V	
Storage temperature	T_stg		-55 °C to +100 °C		Storage as single product.
Operating temperature	T_use		-20 °C to +70 °C		
Frequency tolerance	f_tol		B: ±50 × 10 <sup>-6</sup> C: ±100 ×	10 <sup>-6</sup>	-20 °C to +70 °C
Current consumption	Icc	25 m.	25 mA Max.		No load condition
Disable curren	sable curren I_dis		0 mA Max. 10 mA Max.		OE=GND (PTG,PHG,PCG)
Stand-by current	I_std		— 50 μA Max		ST =GND (SCG)
Symmetry	SYM	— 45 % to 55 %		50 % Vcc level, L_CMOS=25 pF	
Symmetry		40 % to 60 %		1.4 V level, L_CMOS=25 pF	
Output valtage	Vон	2.4 V Min.	_	Vcc-0.4 V Min.	IOH=-8 mA
		_	Vcc-0.4 V Min.		IoH=-16 mA
Output voltage	Vol			0.4 V Max.	IoL=8 mA
		0.4 \	/ Max.	_	IoL=16 mA
Output load condition	L_CMOS	25 pF Max.			
Input voltage	VIH	2.0 V Min.		70 % Vcc Min.	OE Terminal or ST Terminal
	VIL	0.8 V Max.		20 % Vcc Max.	
Rise time / Fall time	tr / tf	_	3.4 ns Max.	4 ns Max.	20 % Vcc to 80 % Vcc level, L_CMOS ≤ 25 pF
		2.4 ns Max.		_	TTL load:0.4 V to 2.4 V level, L_CMOS ≤ 25 pF
Start-up time	t_str	12 ms Max.		t=0 at 90 % Vcc	
Frequency aging	f_aging	$\pm 5  imes 10^{-6}$ / year Max.			+25 °C, Vcc=5.0 V/ 3.3 V, First year

<sup>\*1 4.1250</sup> MHz < fo < 4.4336 MHz, 8.2500 MHz < fo < 8.8672 MHz, 16.500 MHz < fo < 17.7344 MHz : Unavailable



#### **Specifications (characteristics)**

		Specifications			
Item	Symbol	SG-636 PTW / STW	SG-636 PHW / SHW	SG-636 PCW / SCW	Conditions / Remarks
		SG-645 PTW / STW	SG-645 PHW / SHW	SG-645 PCW / SCW	
Output frequency range	fo	3:	2.001 MHz to 135.000 MH	<del>l</del> z	Please contact us about available frequencies.
Supply voltage	Vcc	5.0 V	±0.5 V	3.3 V ±0.3 V	
Storage temperature	T_stg	SG-636***:-55 °C	to +100 °C / SG-645***:	-55 °C to +125 °C	Storage as single product.
O	T_use	-20 °C to +70 °C			
Operating temperature		— -40 °C to +85 °C		SG-645PCW / SCW Only	
Frequency tolerance	f_tol	B: $\pm 50 \times 10^{-6}$ $C^{-2}$ : $\pm 100 \times 10^{-6}$		-20 °C to +70 °C	
		-	_	M: $\pm 100 \times 10^{-6}$	-40 °C to +85 °C : SG-645PCW / SCW Only
Current consumption	Icc	45 mA Max.		28 mA Max.	No load condition( Max. frequency range )
Disable current	I_dis	30 mA Max.		16 mA Max.	OE=GND (PTW,PHW,PCW)
Stand-by current	I_std	50 μA Max.			ST =GND (STW,SHW,SCW)
Symmetry	SYM	— 40 % to 60 %		50 % Vcc level, L_CMOS=Max.	
Symmetry	STW	40 % to 60 %		1.4 V level, L_CMOS=Max.	
	Voн	Vcc-0.4 V Min.			IOH=-16 mA(PTW, STW, PHW, SHW)
Output voltage	VOH				/-8 mA(PCW, SCW)
Output voltage	Vol	0.4 V Max.			IoL= 16 mA(PTW , STW , PHW , SHW) / 8 mA(PCW , SCW)
Output load condition (TTL)	L_TTL	5 TTL Max.	_	_	fo≤ 90 MHz, Max.Supply voltage.
Output load condition (CMOS)	L_CMOS	15 pF Max.		Max.frequency, Max.Supply voltage.	
Input voltage	ViH	2.0 V Min.		70 % Vcc Min.	OE Terminal or ST Terminal
	VIL	0.8 V Max. 20 % Vcc Max.		20 % Vcc Max.	OE Terminal of \$1 Terminal
Rise time / Fall time	tr / tf	— 4 ns Max.			20 % Vcc to 80 % Vcc level, L_CMOS ≤ Max.
		4 ns Max.	_	_	0.4 V to 2.4 V level
Start-up time	t_str	10 ms Max.			Time at minimum supply voltage to be 0 s
Frequency aging	f_aging	$\pm 5 \times 10^{-6}$ / year Max. +25			+25 °C, Vcc=5.0 V / 3.3 V, First year

<sup>\*2</sup> SG-636 series "C" tolerance : 40 MHz<fo≤135 MHz

Product Name (Standard form) SG-645 P T W 135.000000MHz B 1 @3 4

①Model ②Function (P: Output enable, S:Standby)

⑤Frequency tolerance

OE pin = "H" or "open": Specified frequency output.
OE pin = "L": Output is high impedance.

ST pin (STW, SHW, SCW,SCG)
T pin = "H" or "open" : Specified frequency output.
T pin = "L" : Output is low level (weak pull - down),oscillation stops.

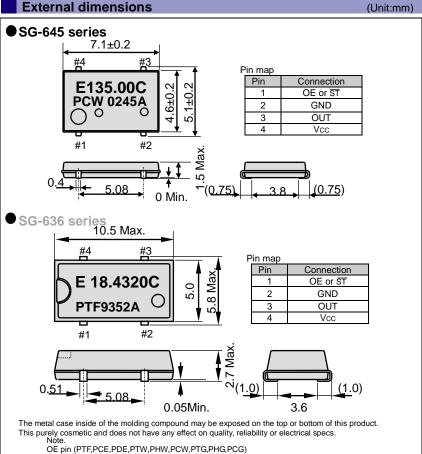
③Supply voltage		
D	2.5 V Typ.	
С	3.3 V Typ.	
T,H	5.0 V Typ.	

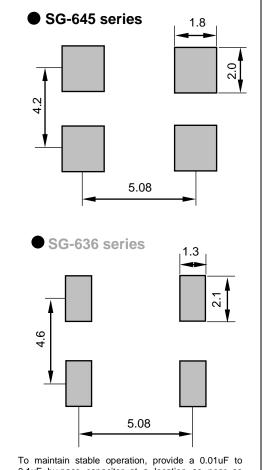
⑤Frequency tolerance			
В	±50 × 10 <sup>-6</sup> / -20 to +70°C		
С	±100 × 10 <sup>-6</sup> / -20 to +70°C		
М	±100 × 10 <sup>-6</sup> / -40 to +85°C		

(Unit:mm)

Footprint (Recommended)

#### External dimensions





To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

# PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

#### **WORKING FOR HIGH QUALITY**

In order provide high quality and reliable products and services than meet customer needs.

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ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



- ► Complies with EU RoHS directive.
  - \*About the products without the Pb-free mark.

    Contains Pb in products exempted by EU RoHS directive.

    (Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



▶ Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc).

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